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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/814,255	03/21/2001	Shunpei Yamazaki	07977-107002	5578

7590

03/31/2003

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EXAMINER

NADAV, ORI

ART UNIT

PAPER NUMBER

2811

DATE MAILED: 03/31/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/814,255

Applicant(s)

YAMAZAKI ET AL.

Examiner

ori nadav

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-- The MAILING DATE of this communication appears in the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 10 February 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1,2,4-6,8,9,11,12,14,15,19-21,23-25 and 27 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1,2,4-6,8,9,11,12,14,15,19-21,23-25 and 27 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) 8.
- 4) ☐ Interview Summary (PTO-413) Paper No(s). _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other:

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DETAILED ACTION

Claim Objections

1. Claims 1-2, 4-6, 8-9, 11-12, ~~14~~-15, 19-21, 23-25 and 27 are objected to because of the following informalities: Claims 1, 5, 9, 12, 15, 20 and 24 recite the limitation an alloy existing at a boundary between the conductive layer and the wiring. It is unclear how a boundary can exist between the conductive layer and the wiring if an alloy is located therebetween.

Appropriate correction is required.

Claim Rejections - 35 USC § 103

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which the subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

3. Claims 1-2, 4-6, 8-9, 11-12, 14-15, 19-21, 23-25 and 27 are rejected under 35 U.S.C. 103(a) as being unpatentable over Lee et al. (5,589,713) in view of Applicant Admitted Prior Art (AAPA).

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Lee et al. teach in figure 11 and related text (column 11, lines 35-64 and column 9, line 3 to column 10, line 57) a semiconductor device comprising at least one transistor over a substrate comprising a conductive layer 106, 110 comprising aluminum, an interlayer insulating film 108 formed on the conductive layer 106, a contact hole formed through the interlayer insulating film; a wiring 112 electrically connected with the conductive layer in the contact hole, and an alloy comprising at least one selected from the group consisting of tin, gallium, zinc, indium, and antimony⁵⁶ existing at least in the contact hole and at a boundary between the conductive layer and the wiring and/or a vicinity thereof (column 7, lines 33-40, column 9, lines 30-43).

Lee et al. do not teach using the wiring structure in a thin film transistor.

AAPA teaches in figure 1 and related text a semiconductor device comprising at least one thin film transistor over a substrate comprising a wiring connection.

It would have been obvious to a person of ordinary skill in the art at the time the invention was made to use the wiring structure of Lee et al. in a thin film transistor, as taught by AAPA, in order to reduce the electromigration effect and to improve the contact resistance between the wiring and the conductive layer. Note that the claimed invention must result in a structural difference between the claimed invention and the prior art in order to patentably distinguish the claimed invention from the prior art. If the prior art structure is capable of performing the intended use, then it meets the claim.

See *In re Casey*, 152 USPQ 235 (CCPA 1967) and *In re Otto*, 136 USPQ 458, 459 (CCPA 1963). Furthermore, a recitation which occurs in the preamble is generally not

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accorded any patentable weight where it merely recites the purpose of a process or the intended use of a structure, and where the body of the claim does not depend on the preamble for completeness but, instead, the process steps or structural limitations are able to stand alone. See *In re Hirao*, 535 F.2d 67, 190 USPQ 15 (CCPA 1976) and *Kropa v. Robie*, 187 F.2d 150, 152, 88 USPQ 478, 481 (CCPA 1951).

Regarding claims 4, 8, 11, 14, 19, 20, 23, 24 and 27, AAPA teaches at least one thin film transistor being an active matrix type EL display device formed over a substrate and comprising at least a semiconductor region, a gate electrode, a gate insulating film interposed therebetween; and an interlayer insulating film formed over the thin film transistor.

Response to Arguments

4. Applicant's arguments with respect to claims 1-2, 4-6, 8-9, 11-12, 14-15, 19-21, 23-25 and 27 have been considered but are moot in view of the new ground(s) of rejection.

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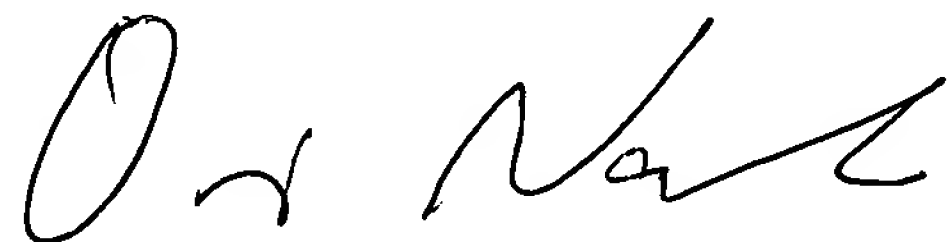
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Papers related to this application may be submitted to Technology center (TC) 2800 by facsimile transmission. Papers should be faxed to TC 2800 via the TC 2800 Fax center located in Crystal Plaza 4, room 4-C23. The faxing of such papers must conform with the notice published in the Official Gazette, 1096 OG 30 (November 15, 1989). The Group 2811 Fax Center number is (703) 308-7722 and 308-7724. The Group 2811 Fax Center is to be used only for papers related to Group 2811 applications.

Any inquiry concerning this communication or any earlier communication from the Examiner should be directed to *Examiner Nadav* whose telephone number is **(703) 308-8138**. The Examiner is in the Office generally between the hours of 7 AM to 4 PM (Eastern Standard Time) Monday through Friday. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas, can be reached at **(703) 308-2772**.

Any inquiry of a general nature or relating to the status of this application should be directed to the **Technology Center Receptionists** whose telephone number is **308-0956**



O.N.
March 25, 2003

ORI NADAV
PATENT EXAMINER
TECHNOLOGY CENTER 2800